



## **2.5G SFP OC-48 LR-2 / STM-16 L16.2 Transceiver**

### **Hot Pluggable, Duplex LC, 1550nm DFB, SMF 100KM, DDM, Multi-Rate**

**Part Number: FSFP-E7-S15-A0DM**



### **Overview**

FSFP-E7-S15-A0DM Small Form Factor Pluggable SFP transceivers are compliant with the current SFP Multi-Source Agreement (MSA) Specification. The high performance uncooled 1550nm DFB transmitter and high sensitivity APD receiver provide superior performance for SONET OC-48 / SDH STM-16 applications up to SMF 100km optical links.

### **Applications**

- SONET OC-48 / SDH STM-16 @2.5G
- SONET OC-12 / SDH STM-4 @622M
- SONET OC-3 / SDH STM-1 @155M
- Gigabit Ethernet @1.25G

### **Features**

- Compatible with SONET OC-48 LR-2 and SDH STM-16 L16.2
- Compatible with SONET OC-12 LR-2 and SDH STM-4 L4.2
- Compatible with SONET OC-3 LR2 and SDH STM-16 L1.2
- Compatible with IEEE802.3z Gigabit Ethernet
- Compliant with INF-8074i SFP MSA
- Multi-Rate 2.5G / 1.25G / 622M / 155M
- Hot Pluggable
- 1550nm DFB laser transmitter
- APD receiver
- Duplex LC connector
- 2-wire interface for management and diagnostic monitor compliant with SFF-8472
- Single +3.3V power supply
- Link distance 100km over SM fiber
- RoHS Compliant



## Laser Safety

- This is a Class 1 Laser Product complies with 21 CFR 1040.10 and 1040.11 except for conformance with IEC 60825-1 Ed. 3., as described in Laser Notice No. 56, dated May 8, 2019.
- Caution: Use of control or adjustments or performance of procedure other than those specified herein may result in hazardous radiation exposure.

## Absolute Maximum Ratings

Parameters	Symbol	Min.	Max.	Unit
Storage Temperature	T <sub>ST</sub>	-40	+85	°C
Storage Relative Humidity	RH	5	95	%
Supply Voltage	V <sub>CC</sub>	-0.5	+4.0	V

## Recommended Operating Conditions

Parameters	Symbol	Min.	Typ.	Max.	Unit
Case Operating Temp. (FSFP-E7-S15-A0DM)	T <sub>OP</sub>	0	-	+70	°C
Case Operating Temp. (FSFP-E7-S15-A0DMi)	T <sub>OP</sub>	-40	-	+85	°C
Supply Voltage	V <sub>CC</sub>	+3.13	+3.3	+3.47	V
Supply Current (FSPP-E7-S15-A0DM)	I <sub>CC</sub>			290	mA
Supply Current (FSPP-E7-S15-A0DMi)	I <sub>CC</sub>			320	mA



## Transmitter Electro-optical Characteristics

$V_{CC} = 3.13V$  to  $3.47V$ ,  $T_{OP} = 0^{\circ}C$  to  $70^{\circ}C$  (FSFP-E7-S15-A0DM);  $T_{OP} = -40^{\circ}C$  to  $85^{\circ}C$  (FSFP-E7-S15-A0DMi)

Parameters	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Data Rate	DR	155	2488		Mb/s	
Optical Launch Power	Po	+2		+5	dBm	1
Optical Center Wavelength	$\lambda_c$	1530	1550	1570	nm	
Spectral Width (-20dB)	$\Delta\lambda$			1	nm	
Side Mode Suppression Ratio	SMSR	30			dB	
Optical Extinction Ratio	ER	8.2			dB	
Optical Eye Mask		ITU-T G.957 STM-16				
Relative Intensity Noise	RIN			-120	dB/Hz	
Differential Data Input Swing	$V_{IN}$	180		1200	mV	
Tx Disable Input Voltage-Low (Tx ON)	TDISV <sub>L</sub>	GND		0.8	V	
Tx Disable Input Voltage-High (Tx OFF)	TDISV <sub>H</sub>	2.0		V <sub>CC</sub>	V	
Tx Fault Output Voltage-Low (Tx Normal)	TFLT <sub>V<sub>L</sub></sub>	GND		0.8	V	
Tx Fault Output Voltage-High (Tx Fault)	TFLT <sub>V<sub>H</sub></sub>	2.0		V <sub>CC</sub>	V	

**Note1:** The optical power is launched into a 9/125 $\mu$ m single mode fiber.



## Receiver Electro-optical Characteristics

$V_{CC} = 3.13V$  to  $3.47V$ ,  $T_{OP} = 0\text{ }^{\circ}C$  to  $70\text{ }^{\circ}C$  (FSFP-E7-S15-A0DM);  $T_{OP} = -40\text{ }^{\circ}C$  to  $85\text{ }^{\circ}C$  (FSFP-E7-S15-A0DMi)

Parameters	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Data Rate	DR	155	2488		Mb/s	
Receiver Sensitivity @2488M	SEN			-28	dBm	1
Receiver Sensitivity @1.25G				-31		2
Receiver Sensitivity @622M				-32		3
Receiver Sensitivity @155M				-32		4
Maximum Receive Power	PR <sub>x</sub> -MAX	-7			dBm	1
Optical Center Wavelength	$\lambda_c$	1260		1610	nm	
LOS De-Assert	LOS <sub>D</sub>			-35	dBm	
LOS Assert	LOS <sub>A</sub>	-45			dBm	
LOS Hysteresis	LOS <sub>HY</sub>	0.5			dB	
Differential Data Output Swing	V <sub>OUT</sub>	600		1000	mV	
Receiver LOS Signal Output Voltage-Low	LOS <sub>VL</sub>	GND		0.8	V	
Receiver LOS Signal Output Voltage-High	LOS <sub>VH</sub>	2.0		V <sub>CC</sub>	V	

**Note1:** Measured with a PRBS 2<sup>23</sup>-1 test pattern @2488Mbps BER<10<sup>-10</sup>.

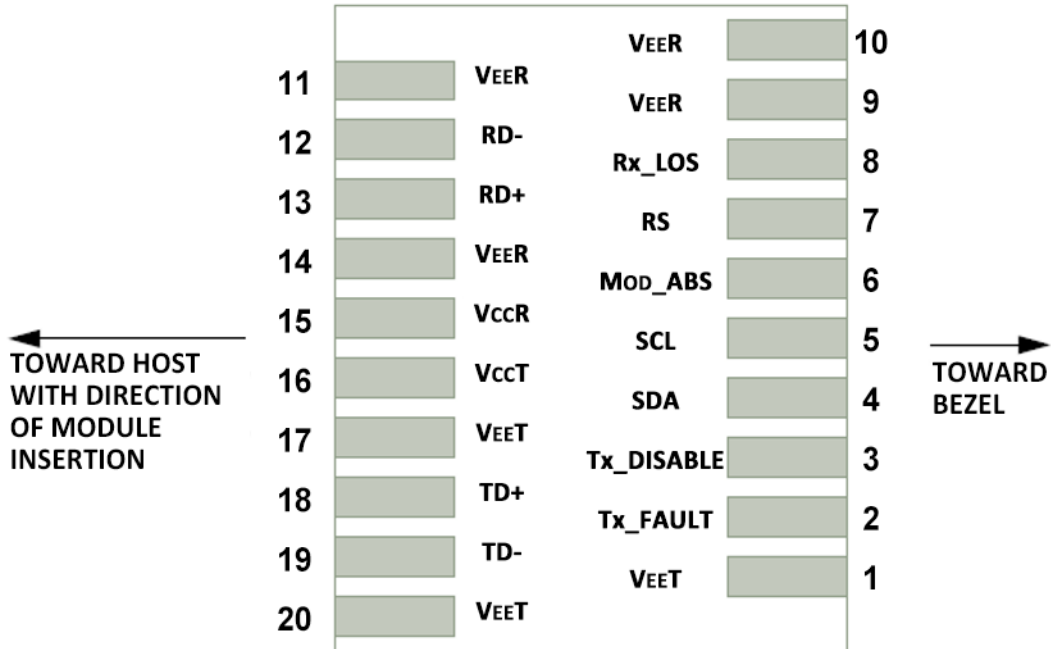
**Note2:** Measured with a PRBS 2<sup>7</sup>-1 test pattern @1.25Gbps BER<10<sup>-12</sup>.

**Note3:** Measured with a PRBS 2<sup>23</sup>-1 test pattern @622Mbps BER<10<sup>-10</sup>.

**Note4:** Measured with a PRBS 2<sup>23</sup>-1 test pattern @155Mbps BER<10<sup>-10</sup>.



## Pin Assignment



Host PCB SFP Pad Assignment Top View

## Pin Description

Pin	Name	Function / Description
1	VEET	Transmitter Ground
2	Tx_FAULT	Transmitter Fault Indication (1)
3	Tx_DISABLE	Transmitter Disable – Turns off transmitter laser output (2)
4	SDA	2-wire Serial Interface Data Line (SDA: Serial Data Signal) (3)
5	SCL	2-wire Serial Interface Clock (SCL: Serial Clock Signal) (3)
6	MOD_ABS	Module Absent, connected to VEET or VEER in the module (3)
7	RS	Rate Select, optional (5)
8	Rx_LOS	Receiver Loss of Signal Indication (4)
9	VEER	Receiver Ground
10	VEER	Receiver Ground
11	VEER	Receiver Ground
12	RD-	Receiver Inverted Data output, Differential LVPECL, AC coupled
13	RD+	Receiver Non-Inverted Data output, Differential LVPECL, AC coupled



14	VEER	Receiver Ground
15	VCCR	Receiver 3.3V Power Supply
16	VcCT	Transmitter 3.3V Power Supply
17	VEET	Transmitter Ground
18	TD+	Transmitter Non-Inverted Data Input, Differential LVPECL, AC coupled
19	TD-	Transmitter Inverted Data Input, Differential LVPECL, AC coupled
20	VEET	Transmitter Ground

**Note1:** Tx Fault is open collector/drain output which should be pulled up externally with a 4.7K~10KΩ resistor on the host board to supply <math>V\_{ccT}+0.3V</math> or <math>V\_{ccR}+0.3V</math>. When high, this output indicates a laser fault of some kind. Low indicates normal operation. In the low state, the output will be pulled to <math><0.8V</math>.

**Note2:** Tx Disable input is used to shut down the laser output per the state table below. It is pulled up within the module with a 4.7K~10KΩ resistor. 1) Low(0~0.8V): Transmitter on; 2) Between(0.8V and 2V): Undefined; 3) High (2.0~ VcCT): Transmitter Disabled; 4) Open: Transmitter Disabled.

**Note3:** These are the module definition pins. They should be pulled up with a 4.7K~10KΩ resistor on the host board to supply less than <math>V\_{ccT}+0.3V</math> or <math>V\_{ccR}+0.3V</math>. MOD\_ABS is grounded by the module to indicate that the module is present.

**Note4:** Rx\_LOS (Loss of signal) is an open collector/drain output which should be pulled up externally with a 4.7K~10KΩ resistor on the host board to supply <math>V\_{ccT}+0.3V</math> or <math>V\_{ccR}+0.3V</math>. When high, this output indicates the received optical power is below the worst case receiver sensitivity (as defined by the standard in use). Low indicates normal operation. In the low state, the output will be pulled to <math><0.8V</math>.

**Note5:** No connect on this module.

## Digital Diagnostic Functions

As defined by the SFP MSA (SFF-8472) Ficer's SFP transceivers provide digital diagnostic functions via a 2-wire serial interface, which allows real-time access to the following operating parameters:

- Transceiver temperature
- Laser bias current
- Transmitted optical power
- Received optical power
- Transceiver supply voltage

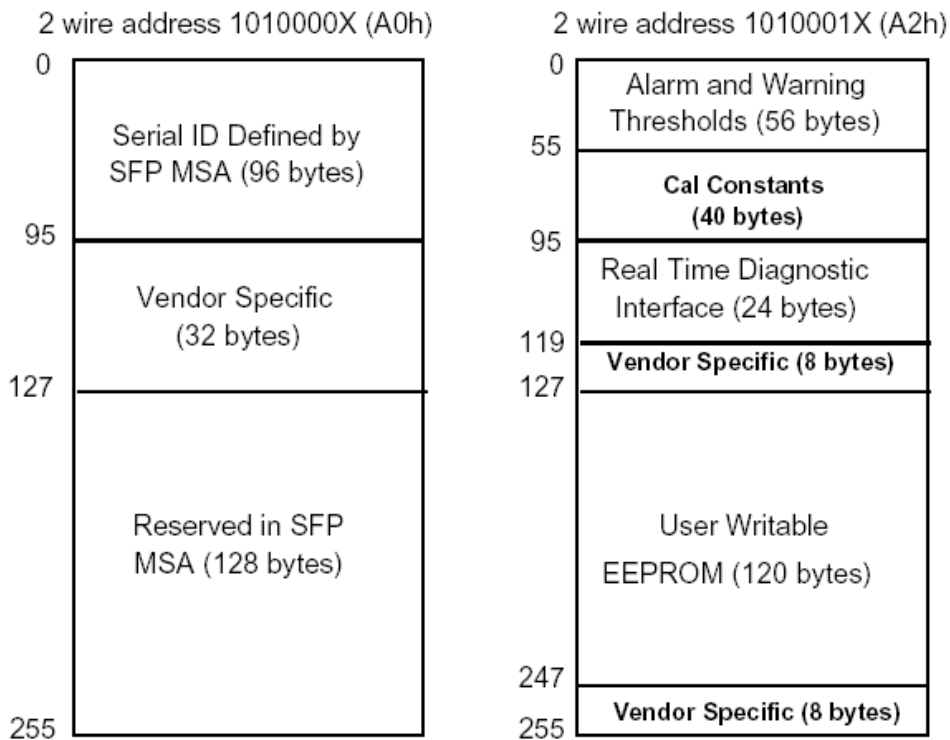
It also provides a sophisticated system of alarm and warning flags, which may be used to alert end-users when particular operating parameters are outside of a factory-set normal range.

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Controller (DDC) inside the transceiver, which is accessed through the 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL pin) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of its memory map that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA pin) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.



For more detailed information including memory map definitions, please see the SFP MSA (SFF-8472) Specification.

### Digital Diagnostic Memory Map

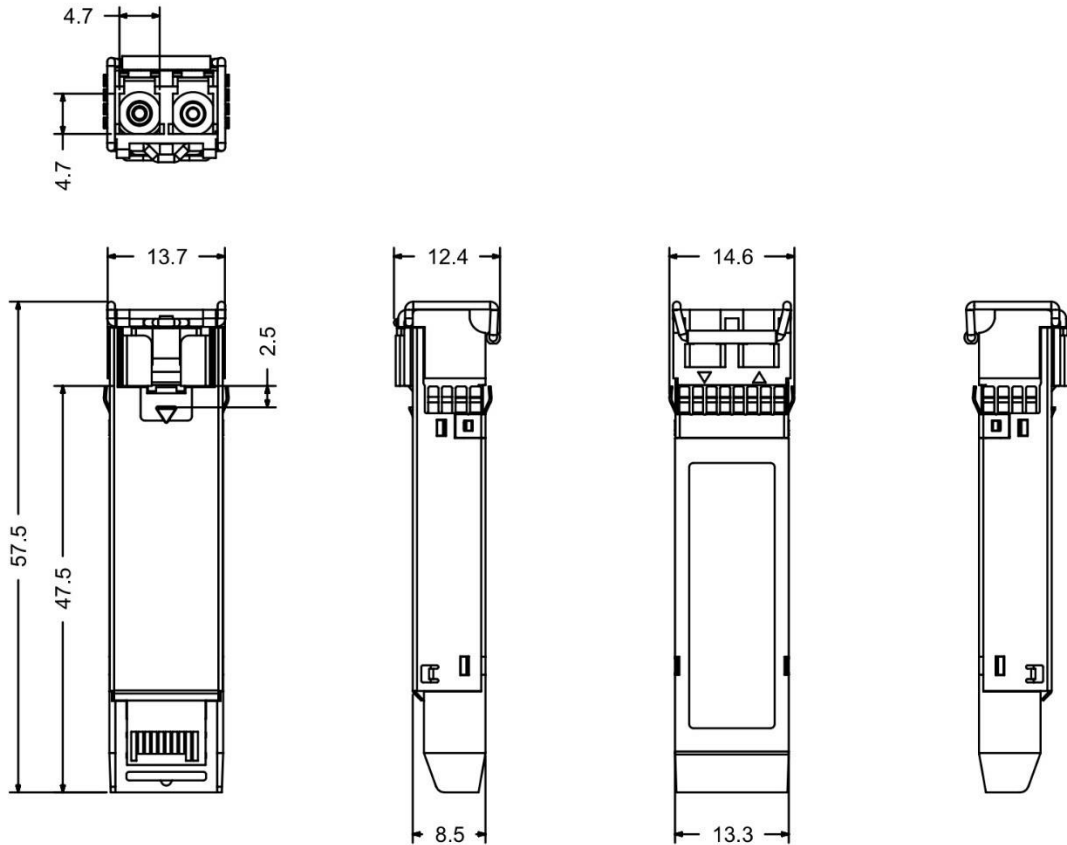


### Digital Diagnostic Monitoring Characteristics

Parameter	Accuracy	Unit	Note
Temperature	±3	°C	Internal Calibration
Supply Voltage	±0.1	V	Internal Calibration
Tx Bias Current	±5	mA	Internal Calibration
Tx Output Power	±3	dB	Internal Calibration
Rx Received Optical Power	±3	dB	Internal Calibration



## Mechanical Dimensions



(All Dimensions are  $\pm 0.20\text{mm}$  Unless Otherwise Specified, Unit: mm)

## Ordering Information

Part No.	Tx	Rx	Link	DDM	Temp.
FSFP-E7-S15-A0DM	1550nm	1260nm	SMF 100km	Yes	0~70°C
FSFP-E7-S15-A0DMi		~ 1610nm			-40~85°C

**Note:** Distances are indicative only. To calculate a more precise link budget based on specific conditions in your application, please refer to the optical characteristics.